



Product Change Notification / SYST-09AUFJ052

Date:

10-Jun-2021

Product Category:

Wireless Modules

PCN Type:

Document Change

Notification Subject:

Data Sheet - ATWILC1000B-MUT IEEE 802.11 b/g/n Link Controller SoC Data Sheet Document Revision

Affected CPNs:

[SYST-09AUFJ052_Affected_CPN_06102021.pdf](#)

[SYST-09AUFJ052_Affected_CPN_06102021.csv](#)

Notification Text:

SYST-09AUFJ052

Microchip has released a new Product Documents for the ATWILC1000B-MUT IEEE 802.11 b/g/n Link Controller SoC of devices. If you are using one of these devices please read the document located at [ATWILC1000B-MUT IEEE 802.11 b/g/n Link Controller SoC](#).

Notification Status: Final

Description of Change: The following is the list of modifications:

Revision B: August 2020:

1. Document section. Minor edits
 - Introduction and Features: Updated peripheral details and added operating conditions
2. Functional Overview section
 - Rearranged sections under this chapter
 - Updated Figure 2-1
 - Updated descriptions of the pins in 2.2 Pin Description
 - Updated pad width in table 2-2.
3. 3.2 Low-Power section: Oscillator Added software implementation related note
4. 4.3 Nonvolatile Memory (eFuse) section: Updated with new information and Figure 4-1
5. WLAN Subsystem section:

- Added a note for FW support of Short GI
 - Updated table 5-1
6. 6. External Interfaces section
- Updated lead sentence
 - Minor edits
 - Added 6.1 Interfacing with the Host Microcontroller
 - Updated Table 6-4
 - Updated Figure 6-4 and Table 6-6
 - Removed I2C Master Interface section and SPI Master Interface section
7. Power Management section
- Updated Figure 7-1
 - Updated RF/AMS core voltage (VREG_BUCK)
8. Electrical Specifications section
- Modified the following sections and regrouped them under Electrical characteristics from other chapters:
- 8.4 Current Consumption in Various Device States
 - 8.5.1 Receiver Performance
 - 8.5.2 Transmitter Performance
 - VDDIO Specification updated
 - Added Section 8.3 DC Electrical Characteristics
9. Reference Design section
- Updated Figure 9-1 and Figure 9-2
 - Added 9.1 Bill of Materials section and updated contents
10. Design Consideration section. Added new chapter
11. Reference Documentation and Supports section: Updated Table 12-1 contents

Revision C: August 2020

1. 4.3 Nonvolatile Memory (eFuse) Errors in Bank numbers fixed in 4.3 Nonvolatile Memory (eFuse) description

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 10 Jun 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[ATWILC1000B-MUT IEEE 802.11 b/g/n Link Controller SoC](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

Terms and Conditions:

If you wish to [receive Microchip PCNs via email](#) please register for our PCN email service at our [PCN](#)

[home page](#) select register then fill in the required fields. You will find instructions about registering for Microchips PCN email service in the [PCN FAQ](#) section.

If you wish to change your PCN profile, including opt out, please go to the [PCN home page](#) select login and sign into your myMicrochip account. Select a profile option from the left navigation bar and make the applicable selections.

Affected Catalog Part Numbers (CPN)

ATWILC1000B-MU-T

ATWILC1000B-MU-T01

ATWILC1000B-MU-T042

ATWILC1000B-MU-Y

ATWILC1000B-MU-Y042

ATWILC1000B-UU-T